



JPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sameer P. Pendharkar Docket No: TI-34637
Serial No: 10/721,567 Conf. No: 5796
Examiner: Ida M. Soward Art Unit: 2822
Filed: 11/25/2003
For: DRAIN EXTENDED MOS TRANSISTOR WITH IMPROVED BREAKDOWN
ROBUSTNESS

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 6-30-05.


Ann Trent

Dear Sir:

Responsive to the Office Action mailed May 4, 2005, in connection with the above identified application, Applicant respectfully submits the following amendments and remarks.